



AD4080 Data Sheet Revision

Data Sheet Specification Comparison

Rev. A (Old)

DIGITAL INPUTS (CNV, CS, SCLK, and SDI)	VDD11 supply domain inputs	0	2.5	
Input Voltage Tolerance				
Logic Levels				
Input Low Voltage, V_{IL}		0	$0.36 \times VDD11$	
Input High Voltage, V_{IH}		$0.73 \times VDD11$	2.5	
DIGITAL INPUTS (GPIOx, DCS, and DCLK)	IOVDD supply domain inputs	0	1.26	V
Input Voltage Tolerance				
Logic Levels				
Input Low Voltage, V_{IL}		0	$0.36 \times IOVDD$	V
Input High Voltage, V_{IH}		$0.73 \times IOVDD$	IOVDD	V
Input Current				
Input Low Current, I_{IL}		-1	+1	μA
Input High Current, I_{IH}		-1	+1	μA
Input Pin Capacitance			4.5	pF

Rev. B (New)

DIGITAL INPUTS (CNV, CS, SCLK, and SDI)	VDD11 supply domain inputs	0	2.5	
Input Voltage Tolerance				
Logic Levels				
Input Low Voltage (V_{IL})		0	$0.36 \times VDD11$	
Input High Voltage (V_{IH})		0.92	2.5	
DIGITAL INPUTS (GPIOx, DCS, and DCLK)	IOVDD supply domain inputs	0	1.26	V
Input Voltage Tolerance				
Logic Levels				
V_{IL}		0	$0.36 \times IOVDD$	V
V_{IH}		0.92	IOVDD	V
Input Current				
Input Low Current (I_{IL})		-1	+1	μA
Input High Current (I_{IH})		-1	+1	μA
Input Pin Capacitance			4.5	pF